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B1B Data Sheet

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## APPLICANTS

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mtt

## \*\* CONTINUING DATA \*\*\*\*\*

mtt

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE

mtt

GRANTED \*\* 06/11/2001

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after
Verified and Acknowledged	Allowance <u>Jefferson</u> Examiner's Signature Initials
STATE OR COUNTRY	CA
SHEETS DRAWING	3
TOTAL CLAIMS	42
INDEPENDENT CLAIMS	4

## ADDRESS

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## TITLE

Linear associative memory-based hardware architecture for fault tolerant ASIC/FPGA work-around

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